

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:	09/560,109	Confirmation No.:	3400
First Named Inventor:	Sallaway, Peter J.	Filing Date:	28 April 2000
Group Art Unit:	2734	Examiner:	Tran, K.
Atty. Docket No.:	M-5628 US		
Title:	Detector For A Gigabit Ethernet Receiver		
Assignee(s):	National Semiconductor Corporation		

Mountain View, California  
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**MAIL STOP NON-FEE AMENDMENT  
COMMISSIONER FOR PATENTS  
PO Box 1450  
Alexandria, Virginia 22313-1450**

**AMENDMENT**

Sir:

In yet further supplement to the Text Amendment submitted 15 September 2003, please amend the above patent application in the following manner.

IN THE SPECIFICATION

Page 55, amend the paragraph beginning at line 5 as follows:

The output signal  $a'_k$  from from pre-equalizer (or feedforward) feedforward section ~~1901~~ ~~1901,  $a'_k$~~ , is input to adder 1902. Adder 1902 subtracts the signal  $a''_k$  from selector ~~1906~~ ~~1906,  $a''_k$~~  from the output signal  $a'_k$  from feedforward section ~~1901, 1902,  $a'_k$~~ . The resulting signal  $a'''_k = a'_k - a''_k$ , ~~signal,  $a'''_k = a'_k - a''_k$~~ , is input to slicer 1903. Slicer 1903 outputs a symbol  $\hat{a}_k$  that is closest to the input signal  $a'''_k$ . The feedback section 1905 (see also feedback section 811 of Figure 8) of decision feedback equalizer 1900 comprises delays ~~1904-1 and 1904-L~~ ~~1904 and 1905~~ and selector 1906. Selector 1906 receives each of L past symbols  $\hat{a}_{k-1}$  through  $\hat{a}_{k-L}$  and uses these symbols to access a lookup table. The lookup table holds values  $\xi_1$  through  $\xi_Q$ . The output signal  $a''_k$  of selector ~~1906~~ ~~1906,  $a''_k$~~ , then is that one

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